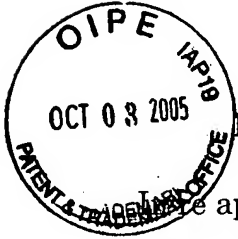


Appl. Ser. No. 10/502,265  
Status Request dated September 28, 2005

2819 *tw*  
PATENT  
Atty. Dkt. No. 81919.0020  
Customer No. 26021



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re application of:

Michitaka KAMEYAMA, et al.

Serial No.: 10/502,265

Filed: July 22, 2004

For: Logical Operation Circuit And Logical  
Operation Method

Art Unit: 2819

Examiner: Not Assigned

Confirmation No.: 9388

**STATUS REQUEST**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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Dear Sir:

This application was filed on July 22, 2004 and receipt acknowledged by the U.S. Patent and Trademark Office ("USPTO") in the Filing Receipt dated March 21, 2005. The last correspondence from the USPTO was a Notice of Publication of Application, which we received on August 3, 2005. However, we are still waiting for a first Office Action.

Please send the undersigned a Status Report on this application.

Respectfully submitted,

HOGAN & HARTSON L.L.P.

Date: September 28, 2005

By:

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